"Engineering Note 100: MCM6830L7 MIKBUG/MINIBUG ROM"
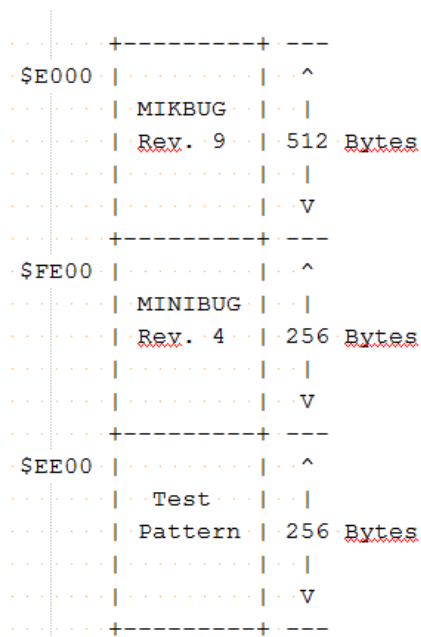By Motorola, Inc.
197x (1976?)

SYSTEMS OVERVIEW

The MIKBUG/MINIBUG ROM provides the user with three separate firmware
programs to interface with a serial asynchronous (start-stop) data
communications device. They are:

    1) MIKBUG Rev. 9
    2) MINIBUG Rev. 4
    3) Test Pattern

The map of the programs is shown in Figure 1-1.

```
              +---------+ ---
  $E000 |         |  ^
         | MIKBUG  |  |
         | Rev. 9  | 512 Bytes
         |         |  |
         |         |  V
              +---------+ ---
  $FE00 |         |  ^
         | MINIBUG |  |
         | Rev. 4  | 256 Bytes
         |         |  |
         |         |  V
              +---------+ ---
  $EE00 |         |  ^
         |  Test   |  |
         | Pattern | 256 Bytes
         |         |  |
         |         |  V
              +---------+ ---
```

   (Fig. 1-1. MIKBUG/MINIBUG ROM Memory Map)

FEATURES

    The more important features of these programs are:

MIKBUG Rev. 9

    A. Memory Loader
    B. Print Registers of Target Program
    C. Print/Punch Dump
    D. Memory Change
    E. Go to Target Program
    F. Operates with PIA for the Parallel-to-Serial Interface
    G. Restart/NMI/SWI Interrupt Vectors

MINIBUG Rev. 4

    A. Memory Loader

B. Memory Change
C. Print Registers of Target Program
D. Go to Target Program
E. Assumes a UART for the Parallel-to-Serial Interface